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J.H. Fischer 41

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Jonathan H. Fischer

Case: 41

Serial No.: 10/002,028

Filing Date: November 15, 2001

Group: 2828

Examiner: Leith A. Al-Nazer

Title: Optical Source Driver with Improved Input Stage

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Luisa M. Nanki Date: September 29, 2003

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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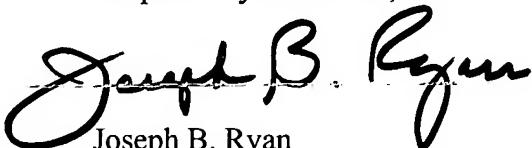
Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. Copies of the listed documents are enclosed.

1. P.R. Gray et al., "Analysis and Design of Analog Integrated Circuits," Third Edition, John Wiley & Sons, Inc., pp. 227-228, 1993.
2. Alan B. Grebene, "Bipolar and MOS Analog Integrated Circuit Design," John Wiley & Sons, Inc., pp. 215-216 and 278-280, 1984.

Please charge the amount of \$180 in accordance with 37 C.F.R. §1.17(p) to **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762**. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error.

The filing of this Supplemental Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,



Date: September 29, 2003

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